

**REGULAR TIME TABLE FOR CLASS WORK :: 2021-22**

<b>YEAR &amp; BRANCH: II-B.Tech., I-Sem, EEE (R20) W.E.F : 01-12-2021</b>					<b>HALL No. 410</b>		
<b>DAY</b>	<b>09:10-10:00</b>	<b>10:00-10:50</b>	<b>11:10-12:00</b>	<b>12:00-12:50</b>	<b>01:40-02:30</b>	<b>02:30-03:20</b>	<b>03:20-04:10</b>
<b>MON</b>	<b>CVT</b>	<b>DLD/ADP</b>			<b>DCMT</b>	<b>UHV(T)</b>	<b>MEFA</b>
<b>TUE</b>	<b>MEFA</b>	<b>ECA</b>	<b>CVT</b>	<b>DLD(T)</b>	<b>DCMT/ECA LAB</b>		
<b>WED</b>	<b>ECA</b>	<b>DLD</b>	<b>CVT(T)</b>	<b>DCMT</b>	<b>MEFA</b>	<b>SPORTS</b>	
<b>THU</b>	<b>ECA</b>	<b>DLD</b>	<b>DCMT</b>	<b>ECA</b>	<b>CVT</b>	<b>UHV</b>	<b>MEFA(T)</b>
<b>FRI</b>	<b>DCMT</b>	<b>UHV</b>	<b>DLD</b>	<b>ECA(T)</b>	<b>DLD/ADP LAB</b>		
<b>SAT</b>	<b>MEFA</b>	<b>DLD</b>	<b>CVT</b>	<b>DCMT(T)</b>	<b>DCMT/ECA LAB</b>		

**TEA BREAK: 10:50 TO 11:10****LUNCH BREAK: 12:50 TO 01:40****SUBJECT****NAME OF THE FACULTY**

<b>CVT</b>	Complex Variables & Transforms	<b>R Maharani</b>
<b>ECA</b>	Electrical Circuit Analysis	<b>M S SHIVA KUMAR</b>
<b>DCMT</b>	DC Machines&Transformers	<b>P PEDDA REDDY</b>
<b>DLD</b>	Didital Logic Design	<b>Dr. K. CHITHAMBARAIAH SETTY</b>
<b>MEFA</b>	Managerial Economics & Financial Analysis	<b>G Tauseef Ahmed</b>
<b>ADP</b>	Application Development with Python	<b>C Rambabu</b>
<b>DCMT LAB</b>	DC Machines&Transformers LAB	<b>P PEDDA REDDY / M. G. SUKANYA</b>
<b>ECA LAB</b>	Electrical Circuit Analysis LAB	<b>M S SHIVA KUMAR / D. B. AMEER SOHAIL</b>
<b>UHV</b>	UNIVERSAL HUMAN VALUES	<b>K. VENKATARAMI REDDY</b>
<b>DLD LAB</b>	Digital Logic Design LAB	<b>T Chakrapani</b>